IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:

   Creta et al.   Examiner: Ernest Unelus

Application No.: 10/828,872   Art Unit: 2181

Filed: 04/20/2004

For: Write Combining Protocol Between
       Processors and Chipsets

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PETITION UNDER 1.137(b)

Sir:

Pursuant to 37 C.F.R. 1.137 (b), applicants hereby request that the application be revived due to unintentional delay in responding to the outstanding non-final office action of 7-16-07.

(1) A reply to the outstanding non-final office action of 7-16-07 is included herewith.

(2) The petition fee is also enclosed herein. However, if there are any additional charges, please charge Deposit Account No. 50-0221.

(3) The delay in filing of the response to the outstanding non-final action of 7-16-07 from the due date of 1-16-08 to 2-13-08 was unintentional. Due to a docketing error the current attorney assigned to the above mentioned application, David P. McAbee, was

unaware of the non-final action of 7-16-07. As a result, a response to the office action was not filed by the final due date of January 16, 2008. The notice of abandonment was mailed on February 5, 2008. David P. McAbee received a voice mail message from Examiner Ernest Unelus inquiring to whether the abandonment of the application was intentional on approximately February 6, 2008. David P. McAbee worked to prepare the reply for submission on February 13, 2008.

(4) There is no terminal disclaimer required under C.F.R. 1.137(d)

Dated: February 13, 2008   /David P. McAbee/Reg. No. 58,104
       David P. McAbee
       Reg. No. 58,104

Intel Corporation
M/S JF3-147
2111 NE 25th Avenue
Hillsboro, OR 97124
Tele – 503-712-4988
Fax – 503-264-1729